

Amendments to the claims are as follows:

1. (Currently Amended) An input circuit of a television tuner, comprising:

a first input terminal to which a television signal is input;
a second input terminal to which an FM broadcasting signal is

input;

an input tuning circuit tuned to the television signal and the FM broadcasting signal,

wherein the first input terminal is connected to ~~an~~the input end of the input tuning circuit;

first and second diodes serially connected between the second input terminal and the input end of the input tuning circuit and simultaneously switched on or off,

wherein the first diode is input to the input end of the input tuning circuit, and

wherein the second diode is provided at ~~at~~the side of the second input terminal;

an inductance element interposed between the second input terminal and the second diode; and

a first capacitance element that forms a low pass filter together with the inductance element ~~in a state~~ when the second diode is switched on and is connected between ~~at~~the connection point of the first diode and the second diode and a ground.

2. (Currently Amended) The input circuit of the television tuner according to Claim 1, further comprising a third diode that is switched on or off simultaneously with first and second switches and is interposed between the inductance element and the second diode; and

a second capacitance element that forms a low pass filter together with the inductance element and the first capacitance element ~~in a state~~ when the second and third diodes are switched on and is connected

between ~~at~~ the connection point of the second diode and the third diode and the ground.

3. (Currently Amended) The input circuit of the television tuner according to Claim 1, further comprising an FM trap circuit provided between the first input terminal and the input end of the input tuning circuit for attenuating an FM broadcasting band; and

a fourth diode connected between the connection point of the first diode and the second diode and ~~an~~ the input end of the FM trap circuit, wherein the fourth diode is switched on or off simultaneously with the first, ~~second, and~~ through third diodes.

4. (Currently Amended) The input circuit of the television tuner according to Claim 3, further comprising a transistor whose emitter is grounded and whose collector is pulled up to a power supply voltage by a feed resistor,

wherein ~~the~~ cathodes of the first, second, and fourth diodes are connected to each other,

wherein ~~the~~ anodes of the second and third diodes are connected to each other,

wherein a bias voltage lower than the power supply voltage is supplied to the anodes of the first, ~~second, third and~~ through fourth diodes,

wherein the cathodes of the first, second, and fourth diodes are connected to the collector of the transistor through a common first resistor,

wherein ~~at~~ the cathode of the third diode is connected to the collector of the transistor through a second resistor, and

wherein the transistor is switched on or off.

Amendments to the Abstract are as follows:

~~An object of the present invention is to switch a television signal or an FM broadcasting signal by a small number of active elements, to input the switched television signal or FM broadcasting signal to an input tuning circuit, to reduce loss in the television signal when the television signal is received, and to reduce interference due to the FM broadcasting signal when the television signal is received.~~

~~According to the present invention, an the~~ In an FM/TV receiver, a first input terminal 4 is connected to ~~an~~the input end 6a of ~~an~~the input tuning circuit 6, first and second diodes 45 and 44 are serially connected between ~~a~~the second input terminal 44 and the input end 6a of the input tuning circuit 6 and are simultaneously switched on or off, the first diode 45 is input to the input end 6a of the input tuning circuit, 6, the second diode 44 is provided at the side of the second input terminal 44, an inductance element 42 is interposed between the second input terminal 44 and the second diode 44, and a first capacitance element 47 that forms a low pass filter together with the inductance element 42 in a state when the second diode 44 is switched on, is connected between the connection point of the first diode 45 and the second diode 44 and a ground.